**Question Bank for PT2**

**Program - CSE Semester – IV**

**Course – MBS Course code - MBS190806**

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|  | **Questions** | **Answer**  **from** | Marks |
| Q.1 | Explain the use of 8288 bus controller in maximum mode of 8086 | PPT | 4 |
| Q.2 | Draw and explain interfacing of 8087 co-processor with 8086 | Notes | 6 |
| Q.3 | Define term multiprocessor system | PPT | 3 |
| Q.4 | Draw and Explain Bus arbitration methods (without adv and disadvantages)  If any single method asks then mention Advantages and disadv.) | PPT/Not  es | 6 |
| Q.5 | Define following terms with suitable diagram   1. Interrupt 2. ISR 3. IVT | PPT | 3/6 |
| Q.6 | Explain the Interrupt process | PPT | 6 |
| Q.7 | List and define the types of interrupt (listing : 1mk, expl: 1mks each) | PPT | 4 |
| Q.8 | Explain dedicated interrupts (each for 2 marks) | PPT | 4/6 |
| Q.9 | Draw architecture of 8259 and explain following blocks:   1. ISR 2. IMR 3. IRR 4. PR   ( only draw – 3mks, draw and explain – 2 mks for diagram and remaining for block explanation) | PPT | 3/4/6 |
| Q.10 | Explain working of single 8259 with 8086 | PPT | 6 |
| Q.11 | Draw architecture of 8255 PPI | PPT | 3 |
| Q.12 | Define the term handshaking | PPT | 3 |
| Q.13 | Draw control word register of 8255 | PPT | 4 |
| Q.14 | Explain **Mode1 Handshake I/O of 8255** for input | PPT | 4 |
| Q.15 | Explain **Mode1 Handshake I/O of 8255** for input | PPT | 4 |
| Q.16 | Problem statements based on 8255 control word register | PPT | 3/4/6 |
| Q.17 | Programs based on string instructions | PPT | 3/4/6 |
| Q.18 | Address generation for 8255 IC (PA,PB,PC,CWR) | PPT | 3/4 |
| Q.19 | Draw and explain architecture of 8255 PPI | PPT | 3/4/6 |
| Q.20 | List any Six/ Eight features of Pentium processor | PPT | 3/4 |
| Q.21 | Draw Pentium superscalar architecture | PPT | 3 |
| Q.22 | State need of branch prediction logic of Pentium processor | PPT | 3 |
| Q.23 | List the application of 8255 | PPT | 4 |
| Q.24 | Explain with neat diagram the Cascade mode of 8259 | PPT | 6 |